

Please note the following alterations to the IM704420-01E.

Page 9-20 “Test result”

- Slope of the data signal:  $\uparrow\downarrow$ , slope of the clock signal:  $\uparrow\downarrow$

Phase difference between the data signal and the clock signal	Standard Deviation $\sigma$ (Jitter)
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	Measured Value	Allowable Range
5.0 ns		0.4 ns or less
10.0 ns		0.4 ns or less

- Slope of the data signal:  $\downarrow\uparrow$ , slope of the clock signal:  $\uparrow\downarrow$

Phase difference between the data signal and the clock signal	Standard Deviation $\sigma$ (Jitter)
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	Measured Value	Allowable Range
30.0 ns		0.4 ns or less

- Slope of the data signal:  $\uparrow\downarrow$ , slope of the clock signal:  $\downarrow\uparrow$

Phase difference between the data signal and the clock signal	Standard Deviation $\sigma$ (Jitter)
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	Measured Value	Allowable Range
30.0 ns		0.4 ns or less

- Slope of the data signal:  $\downarrow\uparrow$ , slope of the clock signal:  $\downarrow\uparrow$

Phase difference between the data signal and the clock signal	Standard Deviation $\sigma$ (Jitter)
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	Measured Value	Allowable Range
5.0 ns		0.4 ns or less
10.0 ns		0.4 ns or less